

FIG. 1

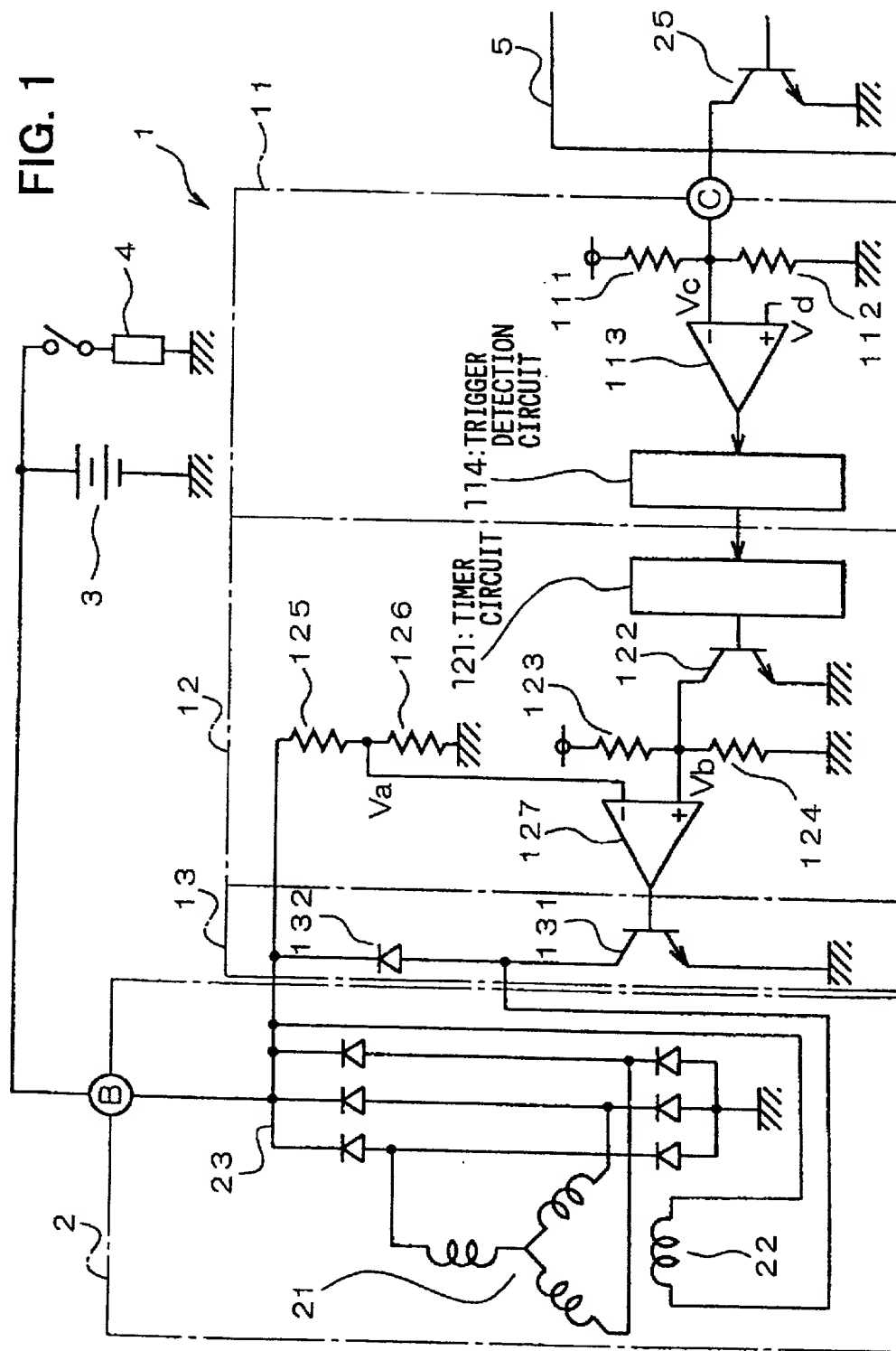


FIG. 2A

TRANSISTOR (25)

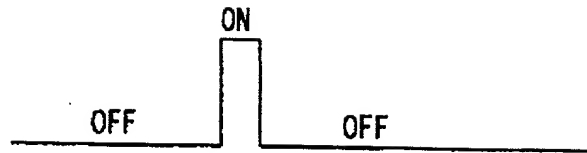


FIG. 2B

C TERMINAL
VOLTAGE



FIG. 2C

OUTPUT OF
VOLTAGE
COMPARATOR (113)

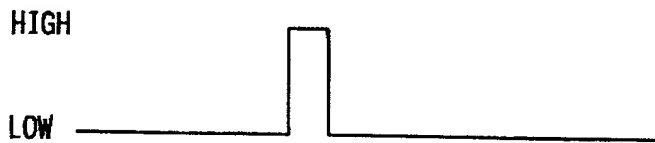


FIG. 2D

OUTPUT OF
TRIGGER DETECTION
CIRCUIT

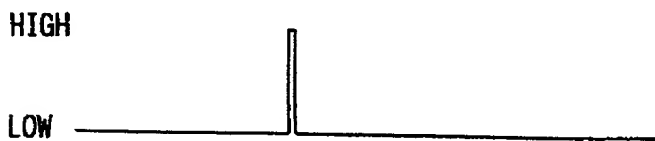


FIG. 2E

OUTPUT OF
TIMER CIRCUIT

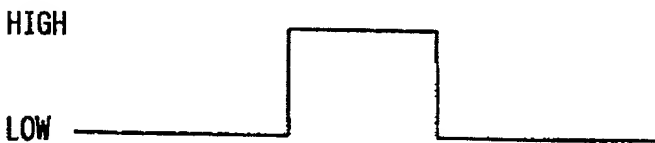


FIG. 2F

ADJUSTMENT
VOLTAGE

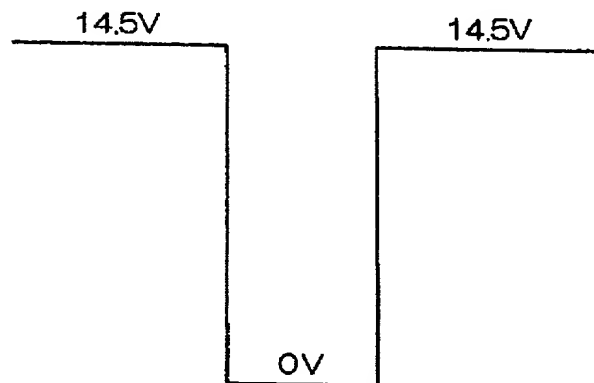


FIG. 3

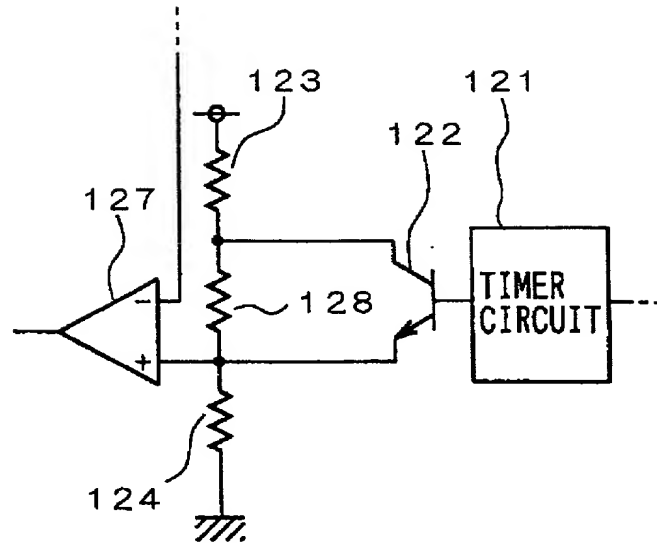


FIG. 12

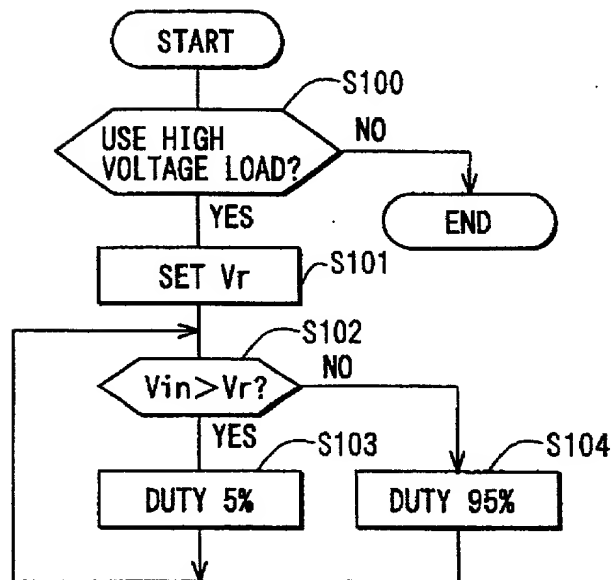


FIG. 4

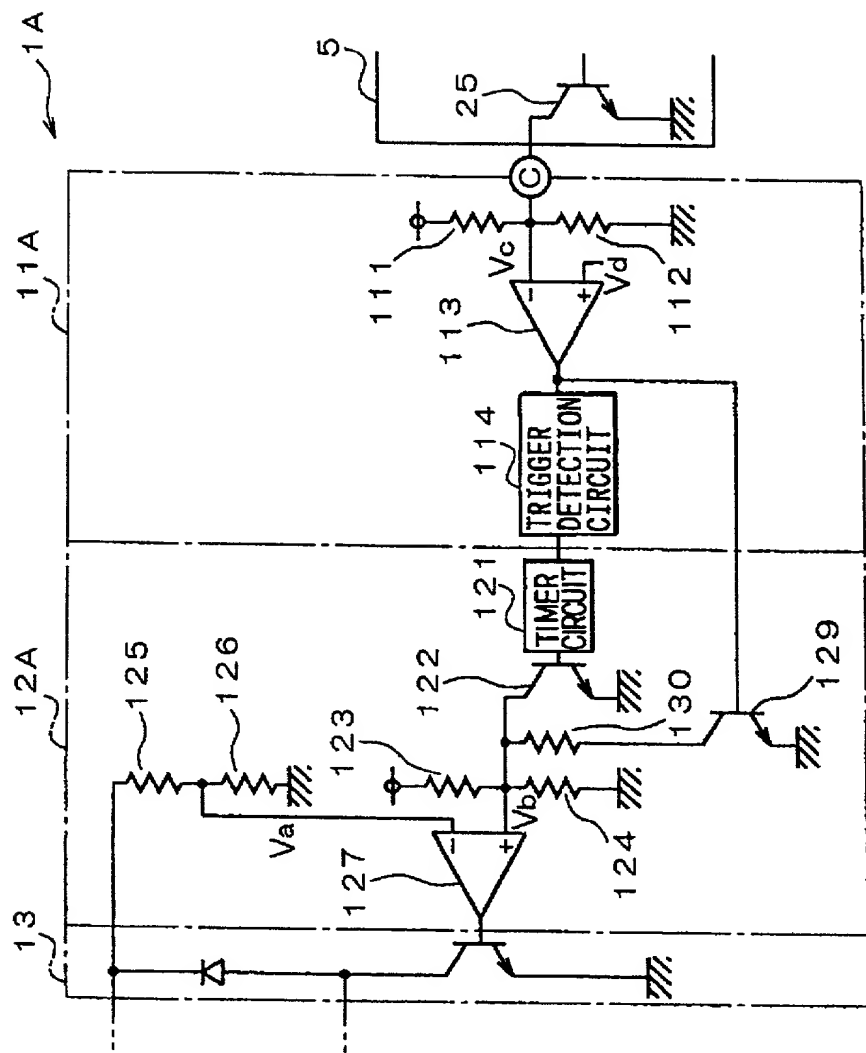


FIG. 5A

TRANSISTOR (25)

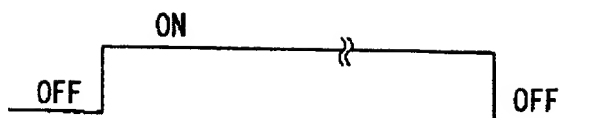


FIG. 5B

C TERMINAL
VOLTAGE



FIG. 5C

OUTPUT OF
VOLTAGE
COMPARATOR (113)



FIG. 5D

OUTPUT OF
TRIGGER DETECTION
CIRCUIT



FIG. 5E

OUTPUT OF
TIMER CIRCUIT



FIG. 5F

TRANSISTOR (122)

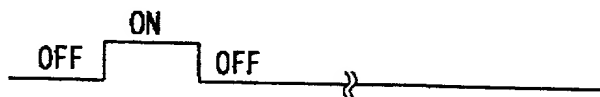


FIG. 5G

TRANSISTOR (129)

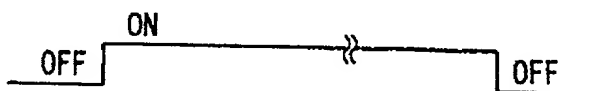


FIG. 5H

ADJUSTMENT
VOLTAGE

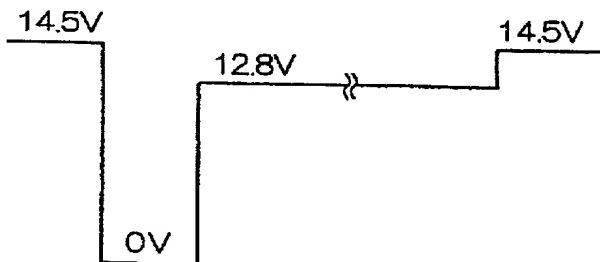


FIG. 6

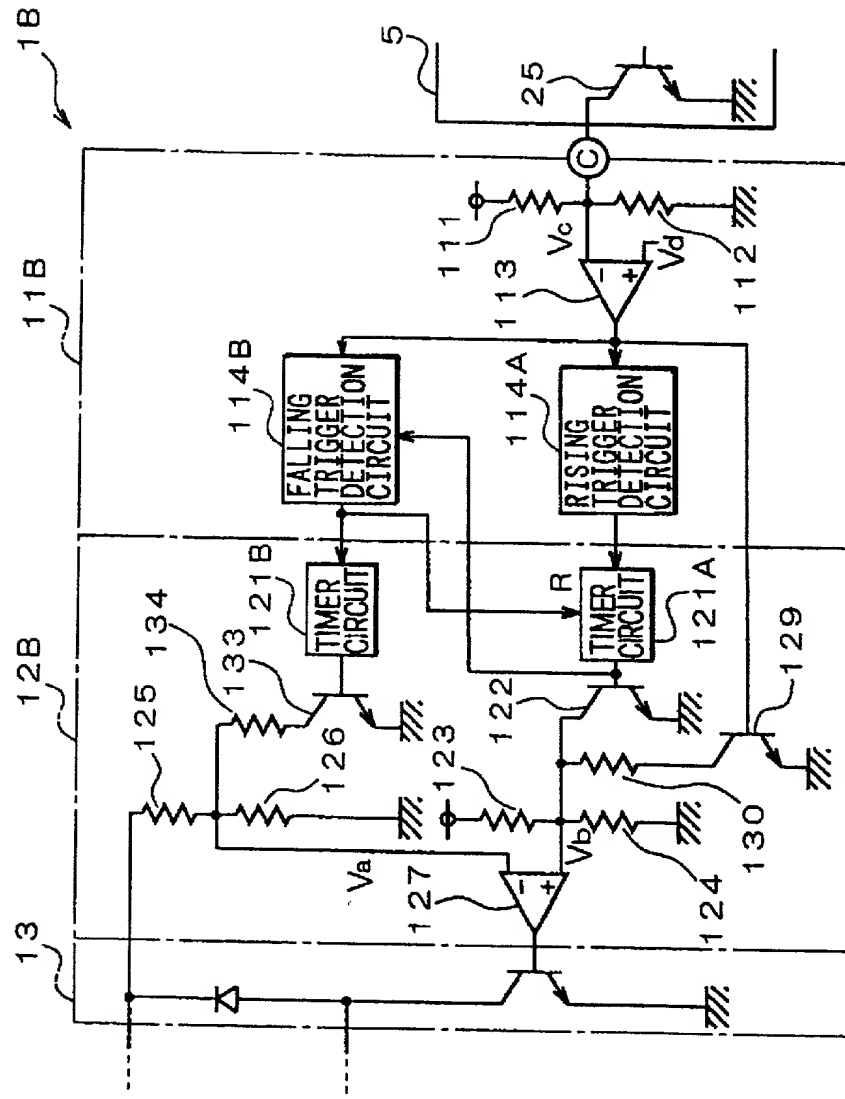


FIG. 7A

TRANSISTOR (25)

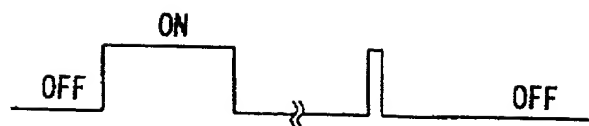


FIG. 7B

C TERMINAL
VOLTAGE



FIG. 7C

OUTPUT OF
VOLTAGE
COMPARATOR (113)



FIG. 7D

OUTPUT OF
TRIGGER DETECTION
CIRCUIT (114A)

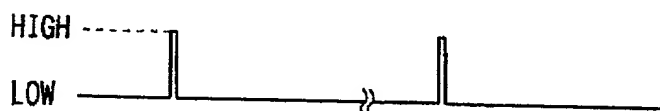


FIG. 7E

OUTPUT OF TIMER
CIRCUIT (121A)

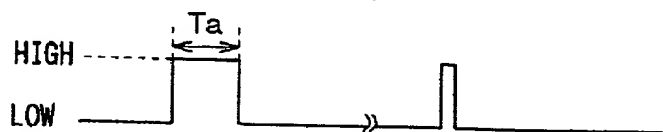


FIG. 7F

OUTPUT OF
TRIGGER DETECTION
CIRCUIT (114B)

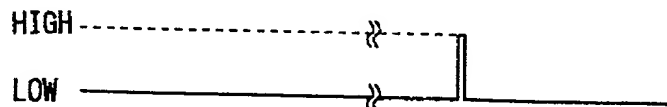


FIG. 7G

OUTPUT OF TIMER
CIRCUIT (121B)

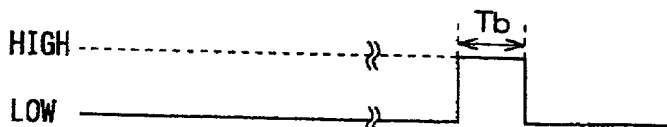


FIG. 7H

ADJUSTMENT
VOLTAGE

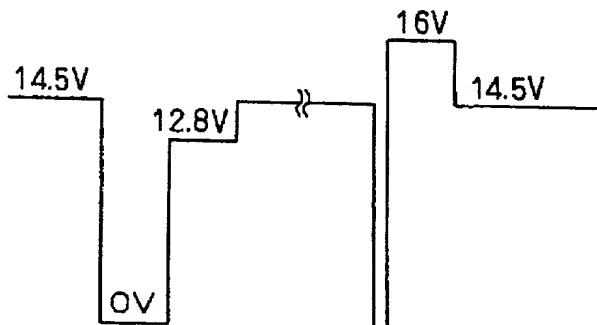


FIG. 8A

TRANSISTOR (25)

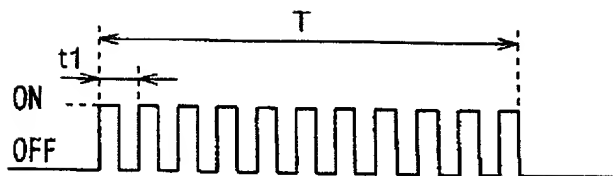


FIG. 8B

C TERMINAL
VOLTAGE



FIG. 8C

OUTPUT OF
VOLTAGE
COMPARATOR (113)

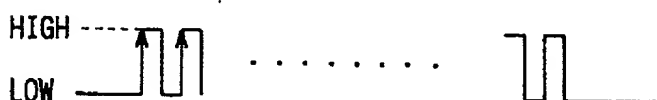


FIG. 8D

OUTPUT OF
TRIGGER DETECTION
CIRCUIT

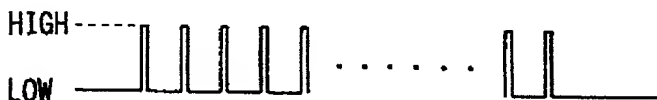


FIG. 8E

TIMER CIRCUIT

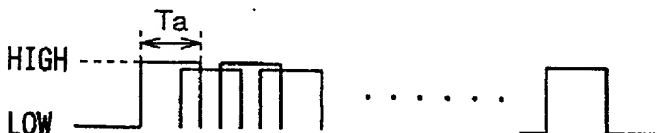


FIG. 8F

OUTPUT OF
TIMER CIRCUIT

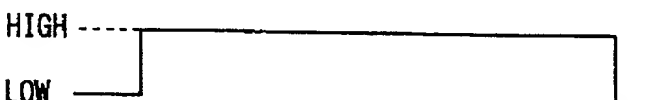


FIG. 8G

ADJUSTMENT
VOLTAGE



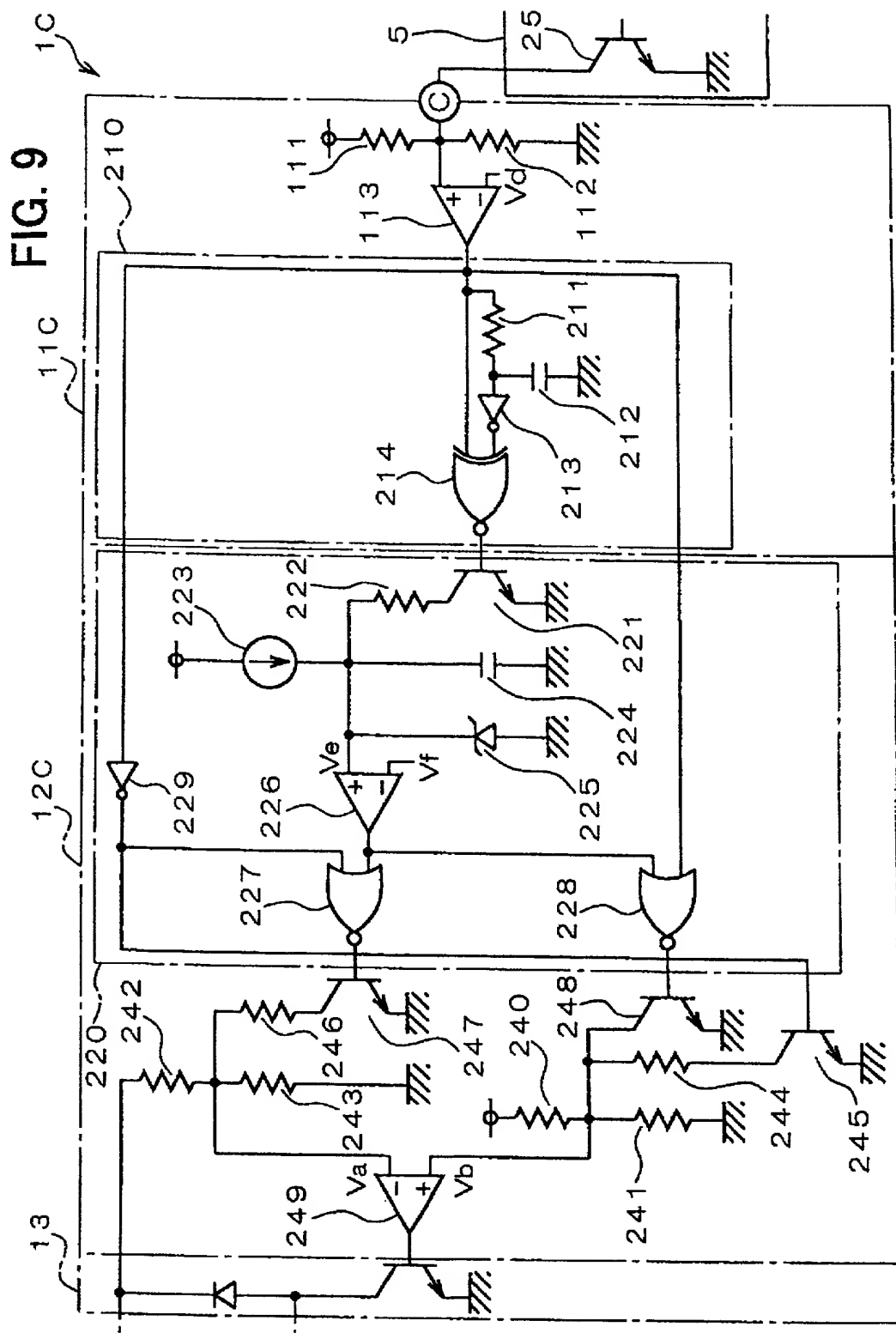


FIG. 10A

C TERMINAL
VOLTAGE

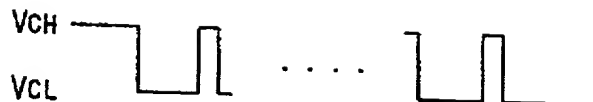


FIG. 10B

OUTPUT OF
VOLTAGE
COMPARATOR (113)

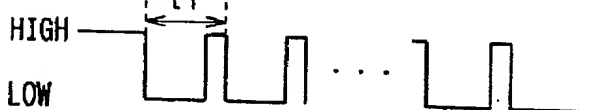


FIG. 10C

OUTPUT OF CHANGE
SIGNAL DETECTION
CIRCUIT (210)

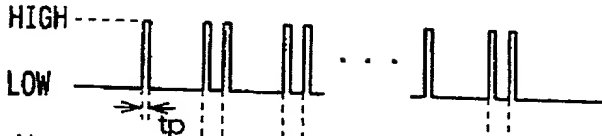


FIG. 10D

CAPACITOR (224)



FIG. 10E

OUTPUT OF
VOLTAGE
COMPARATOR (226)



FIG. 10F

OUTPUT OF NOR
CIRCUIT (228)

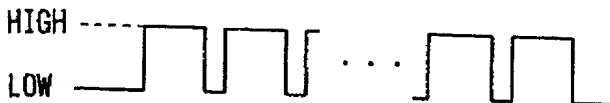


FIG. 10G

OUTPUT OF NOR
CIRCUIT (227)

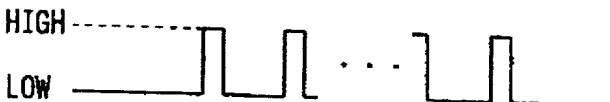


FIG. 10H

ADJUSTMENT
VOLTAGE

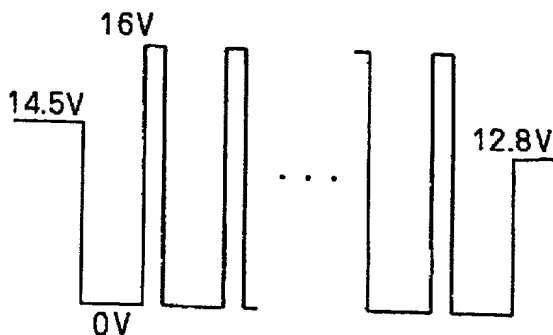


FIG. 11

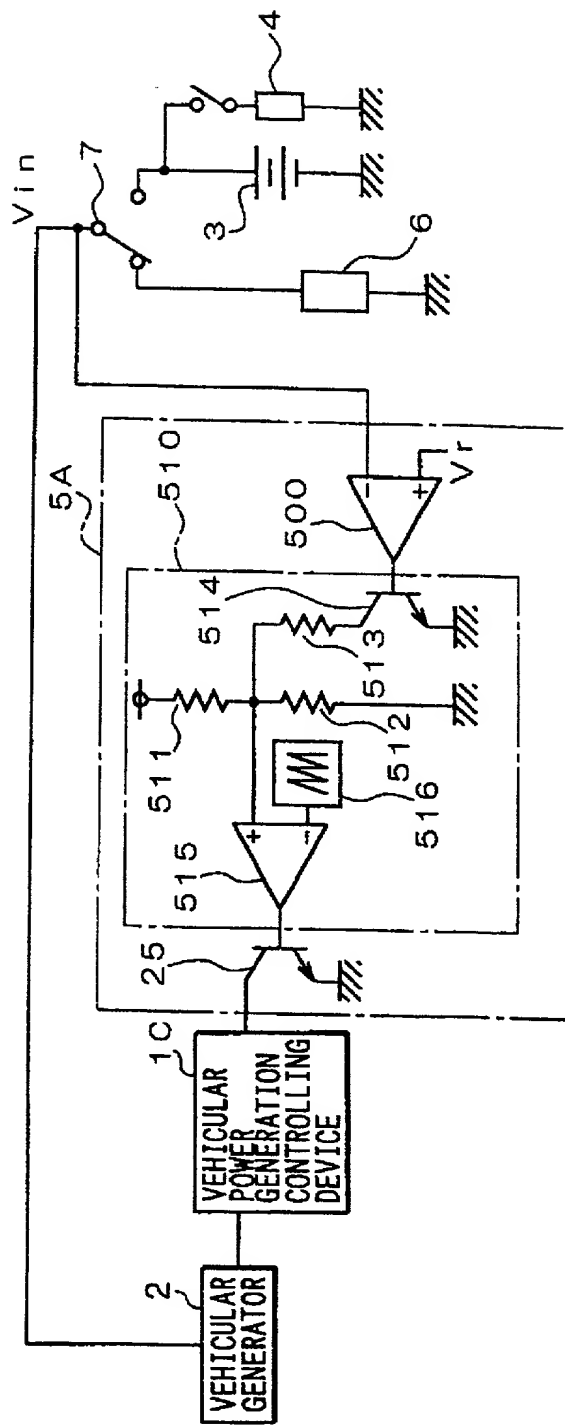


FIG. 13A

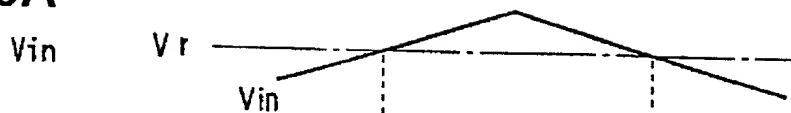


FIG. 13B
SET DUTY
RATIO

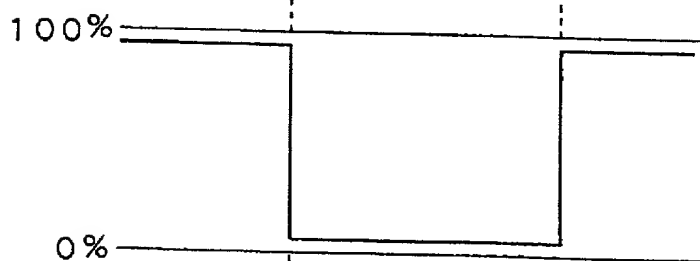


FIG. 13C
C TERMINAL
VOLTAGE



FIG. 13D
OUTPUT
CURRENT



FIG. 15

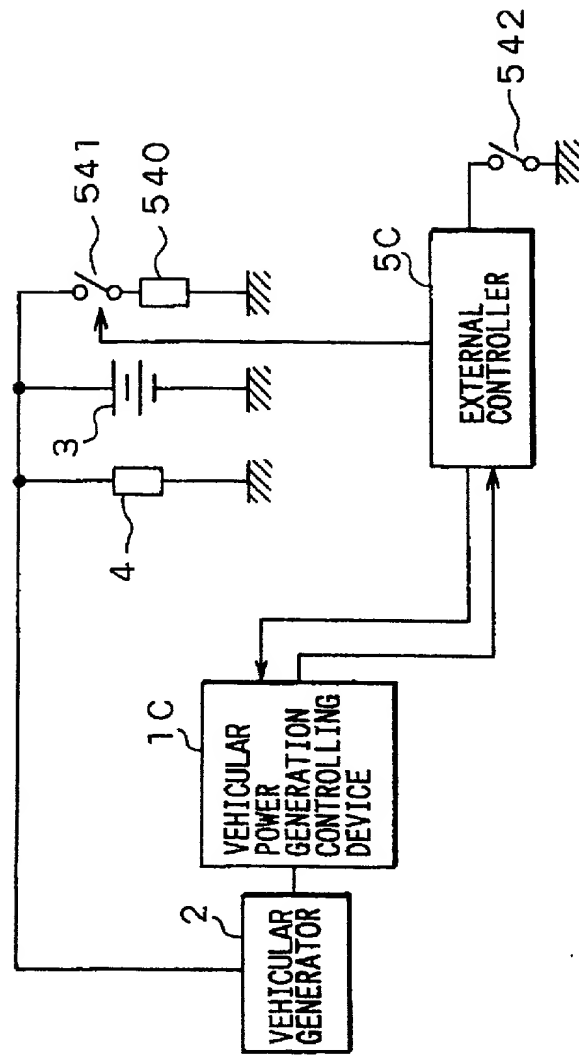


FIG. 16A

FIG. 16A

OPERATION
SWITCH (542)



FIG. 16B

LOAD
SWITCH (541)



FIG. 16C

C TERMINAL
VOLTAGE



FIG. 16D

BATTERY
VOLTAGE



FIG. 16E

POWER
GENERATION
CURRENT

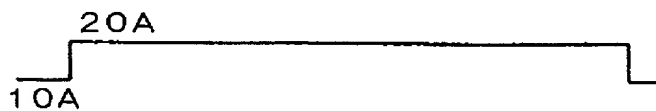


FIG. 16F

BATTERY (3)

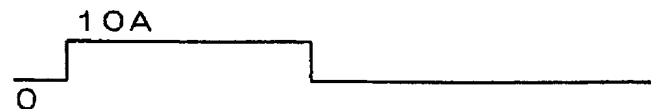


FIG. 16G

LOAD
CURRENT (540)

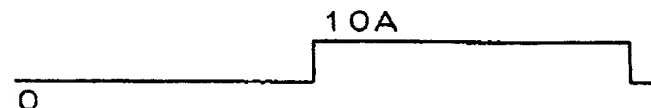


FIG. 17

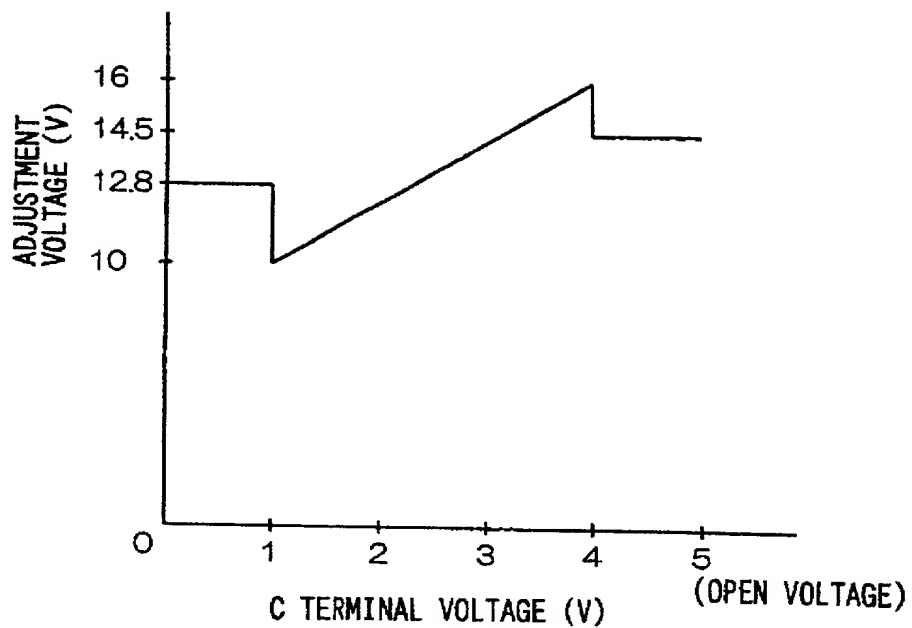


FIG. 21

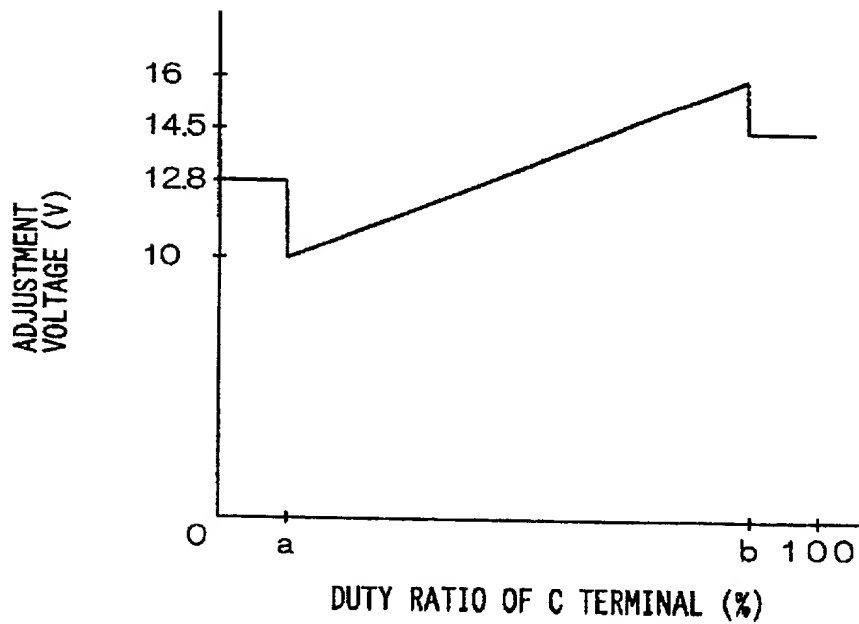


FIG. 18

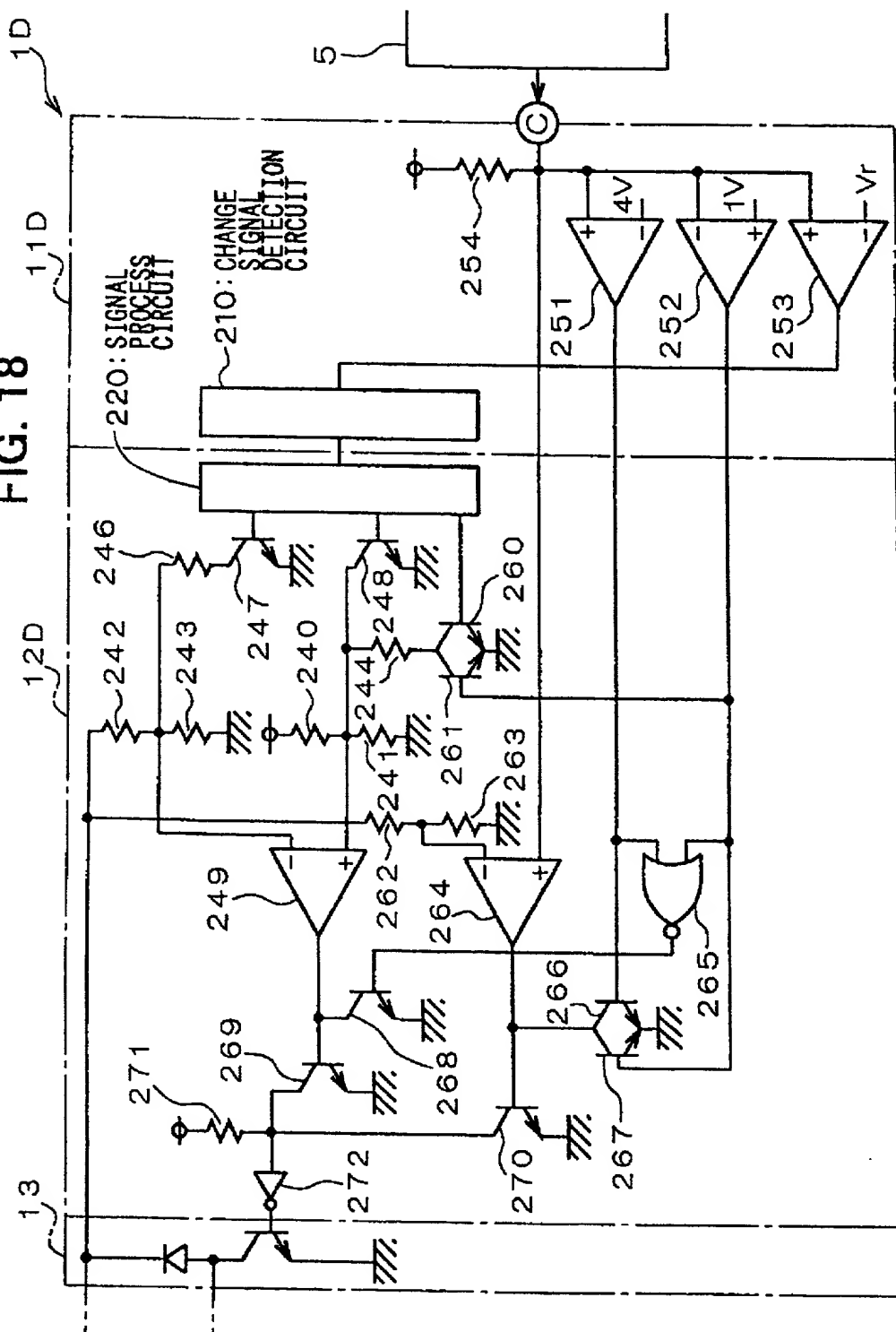
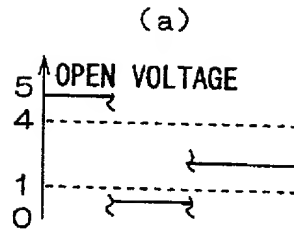


FIG. 19A

C. TERNAL
VOLTAGE



(b)

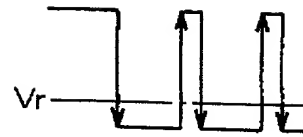


FIG. 19B

OUTPUT OF
VOLTAGE
COMPARATOR (251)



FIG. 19C

OUTPUT OF
VOLTAGE
COMPARATOR (252)



FIG. 19D

OUTPUT OF
NOR CIRCUIT
(265)



FIG. 19E

ADJUSTMENT
VOLTAGE

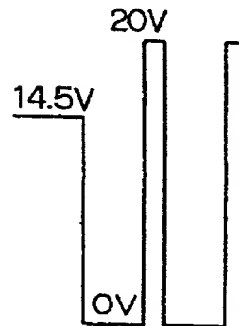
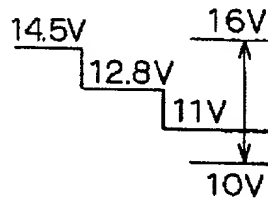


FIG. 20A
C TERMINAL
VOLTAGE

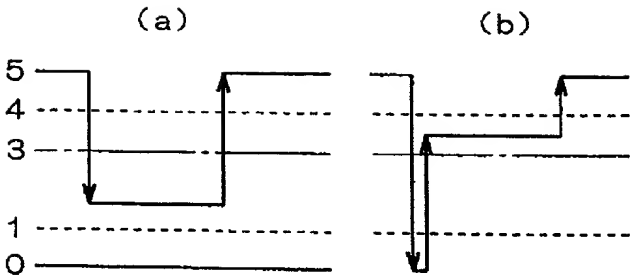


FIG. 20B
OUTPUT OF
CHANGE SIGNAL
CIRCUIT (210)

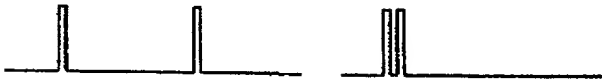


FIG. 20C
OUTPUT OF
SIGNAL PROCESS
CIRCUIT (220)



FIG. 20D
ADJUSTMENT
VOLTAGE

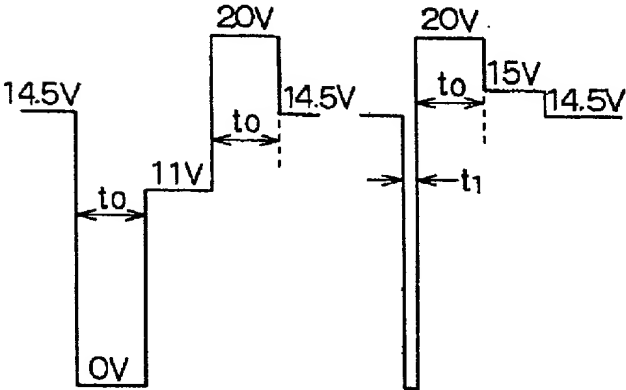
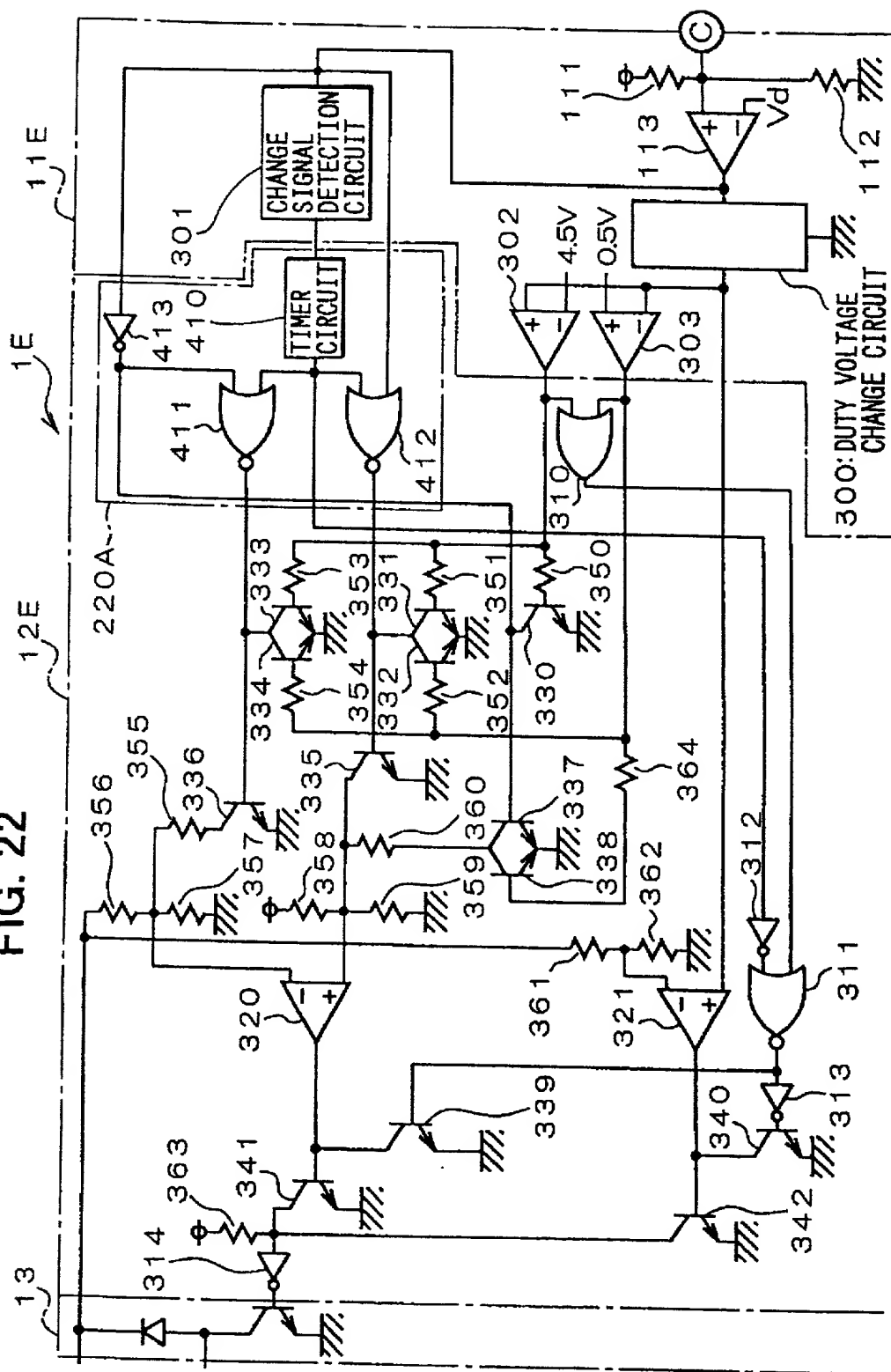


FIG. 22



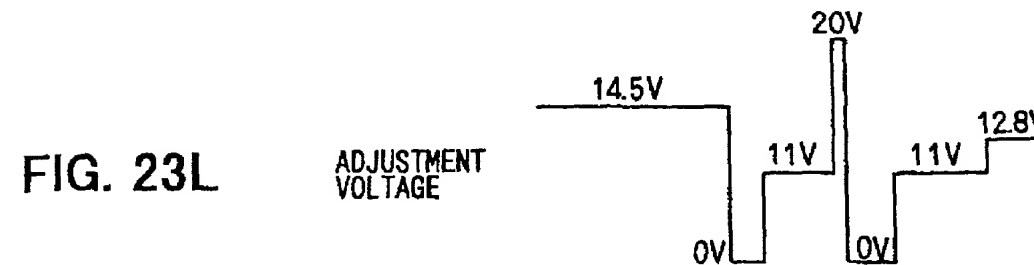
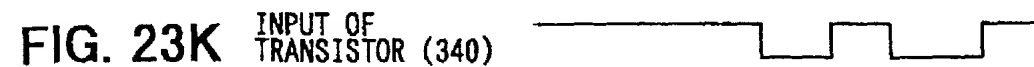
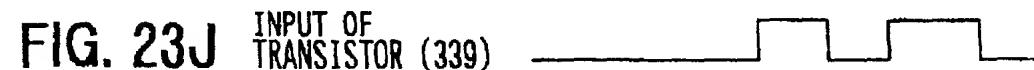
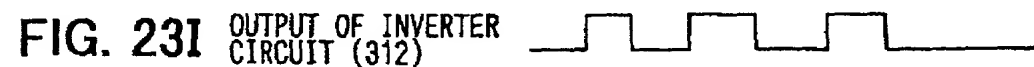
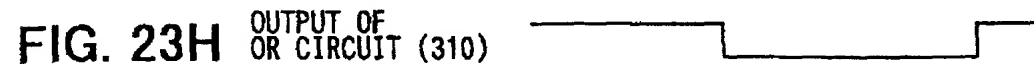
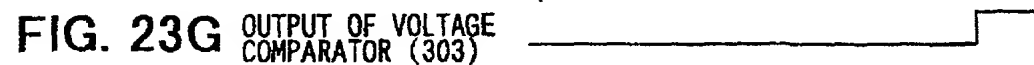
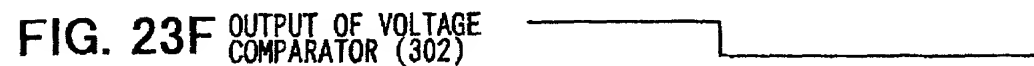
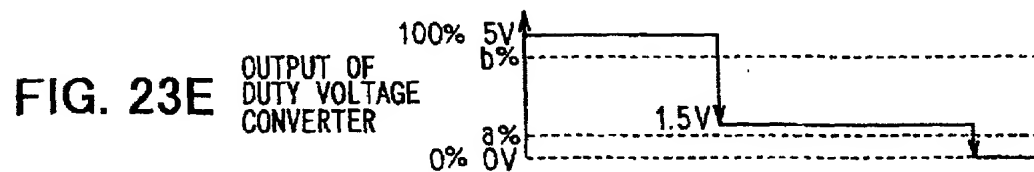
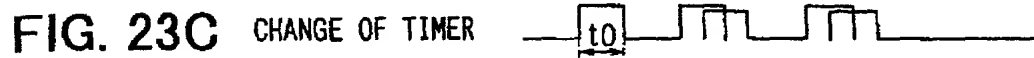
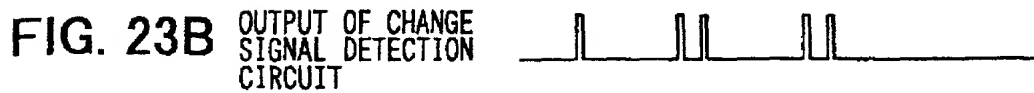
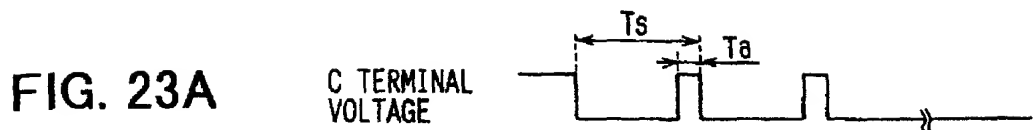


FIG. 24A

C TERMINAL
VOLTAGE

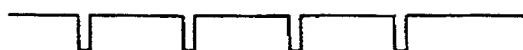


FIG. 24B

OUTPUT OF CHANGE
SIGNAL DETECTION
CIRCUIT



FIG. 24C

CHANGE OF TIMER



FIG. 24D

OUTPUT OF TIMER

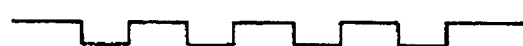


FIG. 24E

OUTPUT OF
DUTY VOLTAGE
CONVERTER

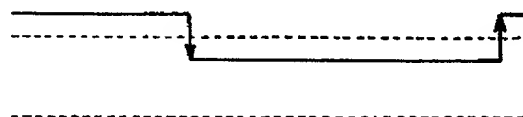


FIG. 24F

OUTPUT OF VOLTAGE
COMPARATOR (302)



FIG. 24G

OUTPUT OF VOLTAGE
COMPARATOR (303)



FIG. 24H

OUTPUT OF
OR CIRCUIT(310)



FIG. 24I

OUTPUT OF INVERTER
CIRCUIT (312)



FIG. 24J

INPUT OF
TRANSISTOR (339)



FIG. 24K

INPUT OF
TRANSISTOR (340)



FIG. 24L

ADJUSTMENT
VOLTAGE

